

B 4 Page 22, line 16, change "Techniques." to --Techniques,
Serial No. 07/337,579, filed April 13, 1989, now abandoned.--

Page 26, line 3, insert a comma --,-- after "204,175" and
insert thereafter --now patent no. 5,095,344,--.

B 5 Page 26, strike all of line 4, and substitute the
following therefore: --Harari, Serial No. 07/337,579, filed April
13, 1989, now abandoned,--.

IN THE CLAIMS:

Add the following new claims:

--68. A method of writing data files into a system of
flash EEPROM cells that are programmable into more than two states
in order to store more than one bit of data per cell, comprising:

temporarily storing, in a cache memory, data files from
a host system intended for the flash EEPROM memory;

writing data files into the cache memory instead of the
flash EEPROM memory in response to a write request from the host
system;

B 6 determining the time since each data file was last
written into said cache memory; and

moving from the cache memory a data file having the
longest time since last written, when additional space for new data
files is required in the cache memory, into the flash EEPROM memory
by programming individual flash EEPROM cells into one of said more
than two programmable states.

69. A method of writing data files into a system of
flash EEPROM cells that are programmable into more than two states
in order to store more than one bit of data per cell, comprising:

temporarily storing, in a cache memory, data files from a host system intended for the flash EEPROM memory;

writing data files into the cache memory instead of the flash EEPROM memory in response to a write request from the host system;

storing, in a tag memory, the identity of data files and the time each data file was last written into said cache memory; and

by reference to the tag memory, moving a data file having the longest time since last written first from the cache memory to the flash EEPROM when additional space for new data files is required in the cache memory, individual flash EEPROM cells being written into one of said more than two programmable states.

70. A method of writing data files into a system of flash EEPROM cells that are programmable into more than two states in order to store more than one bit of data per cell, comprising:

temporarily storing, in a cache memory, data files from a host system intended for the flash EEPROM memory;

in response to a write request from the host system, writing a data file either into the flash EEPROM memory when a previous copy of said data file is not present in the cache memory, or into the cache memory when a previous copy of said data file is present in the cache memory;

moving a data file having the longest time since last written first from the cache memory to the flash EEPROM memory when additional space for new data files is required in the cache memory, thereby substantially reducing the number of actual writes to the flash EEPROM memory; and

wherein a data file is written into the flash EEPROM by programming individual cells thereof into one of said more than two programmable states.

71. A method of writing data files into a system of flash EEPROM cells that are programmable into more than two states in order to store more than one bit of data per cell, comprising:

temporarily storing, in a cache memory, data files from a host system intended for the flash EEPROM memory;

in response to a write request from the host system, writing a data file either into the flash EEPROM memory when said data file is last written after a predetermined period of time, or into the cache memory when said data file is last written within the predetermined period of time; and

moving from the cache memory a data file having the longest time since last written, when additional space for new data files is required in the cache memory, into the flash EEPROM memory by programming individual flash EEPROM cells into one of said more than two programmable states.

72. A method of writing data files into a system of flash EEPROM cells that are programmable into more than two states in order to store more than one bit of data per cell, comprising:

temporarily storing, in a cache memory, data files from a host system intended for the flash EEPROM memory,

storing, in a tag memory, the identity of data files and the time each data file was last written into said cache memory;

in response to a write request from the host system, writing a data file into the flash EEPROM memory when the data file